

Substitute Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-202002	Application No. 10/828,913
<b>Information Disclosure Statement by Applicant</b> (Use several sheets if necessary) (37 CFR §1.98(b))		Applicant Bradley C. Aldrich, et al.	
		Filing Date April 20, 2004	Group Art Unit 2183- C193

**U.S. Patent Documents**

Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
<i>AA</i>	AA	4,748,585	05/1988	Chiarulli, et al.			
<i>AB</i>	AB	5,432,728	07/1995	Curtet			
<i>AC</i>	AC	5,517,436	05/1996	Andreas, et al.			
<i>AD</i>	AD	5,666,169	09/1997	Ohki, et al.			
<i>AE</i>	AE	5,717,923	02/1998	Dedrick			
<i>AF</i>	AF	5,933,797	08/1999	Håakansson, et al.			
<i>AG</i>	AG	6,092,094	07/2000	Ireton			
<i>AH</i>	AH	6,370,630	04/2002	Mizuyabu, et al.			
<i>AI</i>	AI	6,418,527	07/2002	Rozenstein, et al.			
<i>AJ</i>	AJ						
<i>AK</i>	AK						

**Foreign Patent Documents or Published Foreign Patent Applications**

Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
<i>AL</i>	AL	0 654 733	05/1995	Europe				
<i>AM</i>	AM	WO 98/32071	07/1998	WIPO				
<i>AN</i>	AN	WO 99/31601	06/1999	WIPO				
<i>AO</i>	AO							
<i>AP</i>	AP							

**Other Documents (include Author, Title, Date, and Place of Publication)**

Examiner Initial	Desig. ID	Document
<i>AQ</i>	AQ	Bermak, et al., "High-density 16/8/4-bit configurable multiplier", <i>IEE Proc.-Circuits Devices Syst.</i> , 144(5):272-276, (1997).
<i>AR</i>	AR	Kim, et al., "4-way Superscalar DSP Processor for Audio Codec Applications", Proceedings of the 1998 IEEE International Conference on Acoustics, Speech and Signal Processing, ICASSP, '98, Seattle, WA, USA, May 12-15, 1998, IEEE International Conference on Acoustics, Speech and Signal Processing, New York, NY, Vol. 5, Conf. 23, May 12, 1998, pp. 3117-3120.
<i>AS</i>	AS	Singh, et al., "GaAs Low-Power Integrated Circuits for a High-Speed Digital Signal Processor", <i>IEEE Transactions on Electron Devices</i> , 36(2):240-249, (1989).

Examiner Signature <i>D-H. Malzahn</i>	Date Considered <i>6/24/05</i>
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	